Art Unit: 2826

-1-

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:

Yasuhiko TAKEMURA

Serial No. 09/342,235 ) Examiner: A. Sefer

Filed: 06/29/1999

For: SEMICONDUCTOR DEVICE HAVING AT )

LEAST FIRST AND SECOND THIN FILM TRANSISTORS

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 1912, 2011.

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents and Trademarks

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be nade of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

In accordance with 37 C.F.R. § 1.97(e), it is certified that either (1) each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement, or

in a communication from a foreign patent office in a counterpart foreign patent application and no item of information contained in the information disclosure statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this information disclosure statement.

US 5,250,931 and EP 0 342,295 correspond to JP 01-289917.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

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Respectfully submitted,

Eric J. Robinson Reg. No. 38,285

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